

IN THE SPECIFICATION:

The replacement paragraphs are submitted to clarify the specification in view of the Examiner's concerns and the changes to the drawings. Applicant submits that no new matter is injected into the application by way of the substitute paragraphs.

**Please replace the Abstract with the following Abstract:**

IR memory for an EGPRS receiver of a mobile station is provided which receives data from a base station, by means of a data transmission channel, ~~whereby the~~ The IR memory ~~comprises~~ includes a first memory region for the buffering of a particular number of data blocks with a given first data resolution and a second memory region for the buffering of erroneously-decoded data blocks. The second memory region stores the erroneously-decoded data blocks with a second data resolution which is lower than the first data resolution.

**Please replace the paragraph beginning at page 13, line 23 with the following paragraph:**

A step ~~[[S1]]~~ S11 involves reading out the current data sub-blocks from the first memory area **1a** of the IR memory **1** with a data resolution  $R_1$  and depuncturing them with the corresponding puncturing specification.

**Please replace the paragraph beginning at page 13, line 28 with the following paragraph:**

A further step **[[S2]] S12** involves checking whether a further data sub-block with the same block sequence number BSN, the same TFI (temporary frame identity) and a different puncturing scheme P is present. If this is not the case, a step **[[S3]] S13** involves checking whether there is a further data sub-block which has the same block sequence number BSN, the same temporary frame identity TFI and the same puncturing scheme P.

**Please replace the paragraph beginning at page 14, line 1 with the following paragraph:**

If this is the case, a step **[[S4]] S14** involves reading out this data sub-block from the second memory area **1b** of the IR memory **1** with the second data resolution  $R_2$ . The data block read out is scaled upward by  $R_1 \cdot R_2$  bits and depunctured with the corresponding puncturing specification P.

**Please replace the paragraph beginning at page 14, line 9 with the following paragraph:**

A step **[[S5]] S15** involves combining the read-out and depunctured data sub-block with previously combined data sub-blocks.

**Please replace the paragraph beginning at page 14, line 13 with the following paragraph:**

Afterward, a step **[[S6]] S16** involves checking whether or not the number of combined data sub-blocks has exceeded a specific limit value. If this is not the case, the operation returns to step **[[S2]] S12**.

**Please replace the paragraph beginning at page 14, line 17 with the following paragraph:**

In the converse case, the channel decoding of the RLC data block is effected in a step **[[S7]] S17**.

**Please replace the paragraph beginning at page 14, line 21 with the following paragraph:**

A step **[[S8]] S18** involves checking whether the decoding was able to be performed successfully.

**Please replace the paragraph beginning at page 14, line 24 with the following paragraph:**

If the decoding of the RLC data block was successful, the allocated memory area is released for the data and the control information in a step **[[S9]] S19**.

**Please replace the paragraph beginning at page 14, line 28 with the following paragraph:**

If the decoding of the RLC data block could not be concluded successfully, the current data sub-block is stored in the second memory area **1b** of the IR memory **1** with the second data resolution  $R_2$  in a step **[[S10]] S20**.

**Please replace the paragraph beginning at page 15, line 27 with the following paragraph:**

In a step **[[S1]] S21**, the data received from the equalizer of the receiver are stored in a buffer memory of a digital signal processor DSP.

**Please replace the paragraph beginning at page 15, line 31 with the following paragraph:**

A step **[[S2]] S22** involves checking whether all four data bursts of an RLC data block have been received.

**Please replace the paragraph beginning at page 15, line 34 with the following paragraph:**

If all four data bursts associated with same RLC data block are ready for the data processing, the data are de-interleaved in a step **[[S3]] S23**. The header data are subsequently decoded in a step **[[S4]] S24**.

**Please replace the paragraph beginning at page 16, line 4 with the following paragraph:**

A step **[[S5]] S25** involves checking whether the decoding of the header data was concluded successfully.

**Please replace the paragraph beginning at page 16, line 7 with the following paragraph:**

If this is not the case, the current RLC data block is erased in a step **[[S6]] S26**. If, conversely, it is ascertained in a step **[[S5]] S25** that the decoding of the header data was able to be concluded successfully, in a step **[[S7]] S27** the corresponding data sub-blocks are stored in the first memory area **1a** of the IR memory **1** with a data resolution **R<sub>1</sub>**.

**Please replace the paragraph beginning at page 16, line 15 with the following paragraph:**

Figure 12 shows a flowchart for the storage of a new data sub-block in the IR memory **1**. Firstly, a scan IR Mem is carried out in a step **[[S1]] S31**, i.e. a search is made for free memory locations in a control information table.

**Please replace the paragraph beginning at page 16, line 21 with the following paragraph:**

If it is ascertained in a step **[[S2]] S32** that the IR memory **1** is full, a further step

[[S3]] S33 involves carrying out a scan-4-overwrite-same-BSN procedure, which involves overwriting all data block entries for overwriting a data sub-block version with the same block sequence BSN and the same TFI number as the current data sub-block to be stored. If no data sub-block with the same BSN number and the same TFI number can be overwritten and this is ascertained in step [[S4]] S34, all further data block entries are scanned or searched for in a further scan-4-overwrite-other-BSN procedure in a step [[S5]] S35, and a data sub-block version with a BSN and TFI number other than those of the data sub-block that is currently to be stored is overwritten.

**Please replace the paragraph beginning at page 17, line 1 with the following paragraph:**

If it is ascertained in step [[S6]] S36 that this scan overwrite procedure proceeded successfully, the control information table is renewed or updated in a step [[S7]] S37. If one of the three scan procedures carried out in steps [[S1]] S31, [[S3]] S33, [[S5]] S35 was able to be concluded successfully, the control information is updated by a new BSN, TFI, RX quality value and by the new puncturing scheme and also the new modulation coding scheme in step [[S7]] S37. If no free or overwritable memory space is present, an indication signal is transmitted to the base station BS, which indicates to the base that there is no available memory space present in the mobile station MS.

**Please replace the paragraph beginning at page 17, line 14 with the following paragraph:**

After the updating of the control information table in step **[[S7]] S37**, the soft output data resolution is scaled downward from  $R_1$  to  $R_2$  in a step **[[S8]] S38**.

**Please replace the paragraph beginning at page 17, line 18 with the following paragraph:**

The data sub-block is subsequently stored in a step **[[S9]] S39**. The information about the memory allocation conditions are transmitted to the microprocessor of the mobile station in a step **[[S10]] S40**.

**Please replace the paragraph beginning at page 17, line 29 with the following paragraph:**

If one of the scan procedures carried out in steps **[[S1]] S31**, **[[S3]] S33**, **[[S5]] S35** has proceeded successfully, a step **[[S11]] S41** involves checking whether the reception signal quality lies above a specific threshold value.

**Please replace the paragraph beginning at page 17, line 34 with the following paragraph:**

In a step **[[S12]] S42**, the information table is updated and a downscaling of the data resolution to  $R_{2,1}$  bits is subsequently performed in a step **[[S12]] S43**.

**Please replace the paragraph beginning at page 18, line 3 with the following paragraph:**

If, conversely, the data signal reception quality is below the threshold value, the table is correspondingly updated in a step **[[S14]] S44** and the data resolution is downscaled to merely  $R_{2,2} < R_{2,1}$  bits in a step **[[S15]] S45**.

**Please replace the paragraph beginning at page 18, line 8 with the following paragraph:**

The data sub-block is subsequently stored in a step **[[s16]] S46**.

**Please replace the paragraph beginning at page 18, line 10 with the following paragraph:**

Finally, in a step **[[S17]] S47**, the memory allocation conditions are reported to the data processing unit.